

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	21	(tft or thin adj film adj transistor\$1) with (low adj amplitude)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/23 19:59
L2	7	("5748026").URPN.	USPAT	OR	OFF	2006/05/23 19:31
L3	12	("3684900" "4091296" "4137469" "4567380" "4767951" "5012137" "5075581" "5191233" "5283482" "5298808" "5332935" "5646642").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/05/23 19:46
L4	916	(level adj convert\$3 adj circuit)	US-PGPUB; USPAT; USOCR	OR	OFF	2006/05/23 19:47
L5	710	(first adj transistor\$1) near4 (first adj power)	US-PGPUB; USPAT; USOCR	OR	OFF	2006/05/23 19:51
L6	704	(second adj transistor\$1) near4 (second adj power)	US-PGPUB; USPAT; USOCR	OR	OFF	2006/05/23 19:52
L7	488	(first adj bias adj circuit)	US-PGPUB; USPAT; USOCR	OR	OFF	2006/05/23 19:49
L9	601	(second adj bias adj circuit)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/23 19:50
L10	0	1 and 7	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/23 19:50
L11	53	(first adj transistor\$1) same 7	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/23 19:51

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L12	130	(first adj transistor\$1) and 7	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/23 19:51
L13	167	(second adj transistor\$1) and 9	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/23 19:52
L14	103	12 and 13	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/23 19:52
L15	354	(level adj convert\$3 adj circuit) near5 (tft or thin or film or transistor\$1)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/23 19:54
L16	1	14 and 15	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/23 19:53
L17	1377	(level adj convert\$3 adj circuit) and (tft or thin or film or transistor\$1)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/23 19:54
L18	354	15 and 17	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/23 19:54
L19	13	(non\$1differential) adj input adj signal\$1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/23 19:56

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L20	118	(non\$1differential) adj signal\$1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/23 19:56
L21	0	18 and 20	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/23 19:57
L22	6826	(tft or thin adj film or transistor\$1) near4 amplitude	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/23 19:58
L23	0	20 and 22	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/23 19:58
L24	73	17 and 22	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/23 19:58
L25	29	(tft or thin adj film adj transistor\$1) same (low adj amplitude)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/23 19:59
L26	124	(tft or thin adj film adj transistor\$1) and (low adj amplitude)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/23 19:59
L27	6	24 and 26	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/23 20:13

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L28	1	(third adj transistor\$1) with (gate adj electrode\$1)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/23 20:15
L29	3	(third adj transistor\$1)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/23 20:15
L30	91311	first adj switch\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/23 20:17
L31	1954	(first adj switch\$3) same (first adj power)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/23 20:18
L32	279	(first adj switch\$3) same (electrode) same (first adj power)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/23 20:19
L33	279	31 and 32	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/23 20:19
L34	0	27 and 33	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/23 20:20
L35	2	24 and 33	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/23 20:22

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L36	52	(inverter adj circuit) near4 (first adj power) with (second adj power)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/23 20:24
L37	1	24 and 36	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/23 20:24
L38	1	18 and 36	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/23 20:24
L39	33	(inverter adj circuit) and 24	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/23 20:25
L40	2	26 and 39	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/23 20:25